SPECIFICATION

VXT350BVS-04

■ Preliminary Specification

☐ Final Specification

VICTRONIX COMPANY LIMITED

Customer:

Made By:

Checked By:

Approved By:

Quality:

Date:

Note:

Approved By:

Date:

Note:

2. Revision Record

Date	Rev.No.	Page	Revision Items	Prepared
2020-09-29	V0		The first release	LYY

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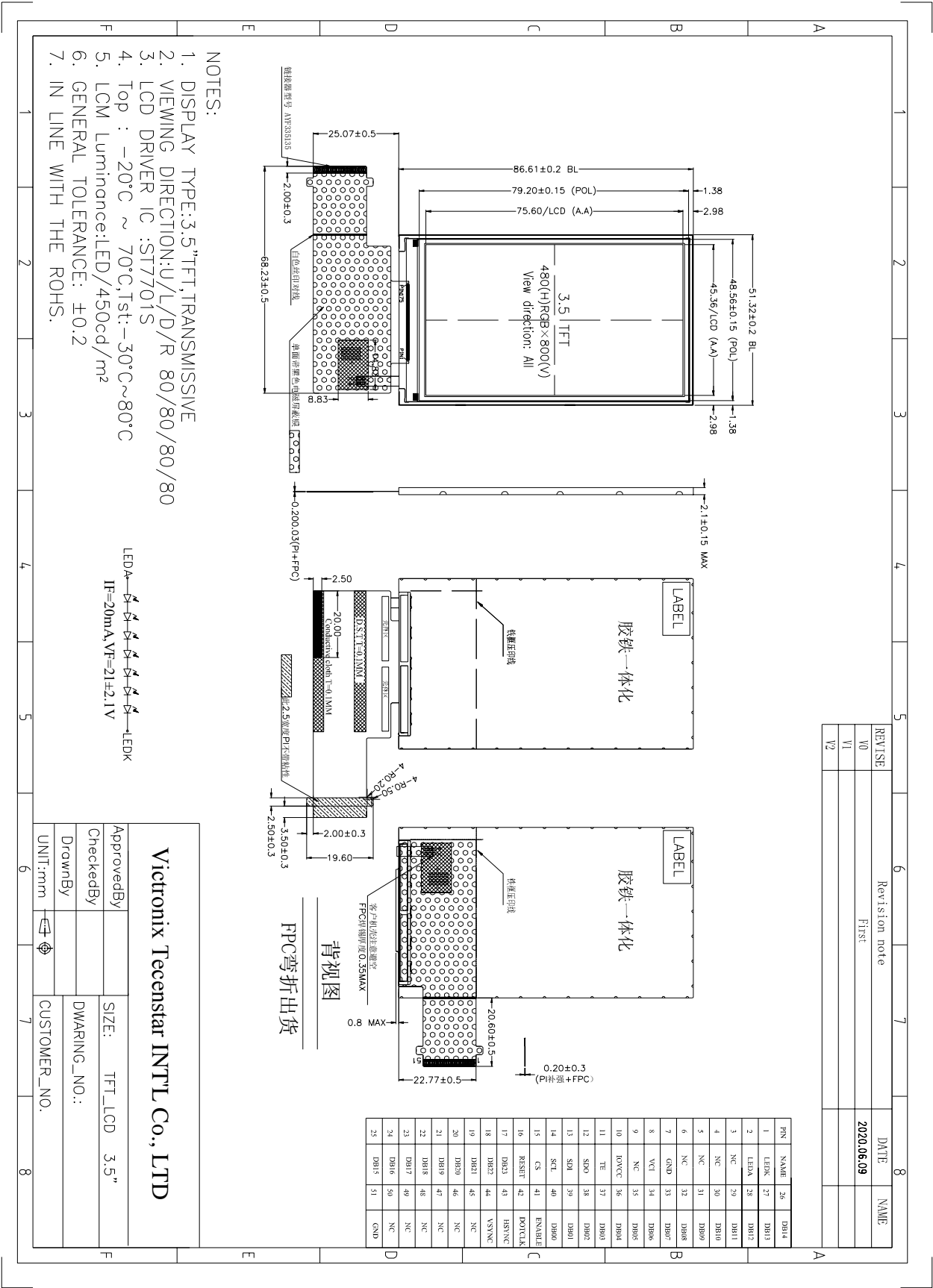
3. General Specifications

VXT350BVS-04 is a TFT-LCD module. It is composed of a TFT-LCD panel, driver IC, FPC,a back light unit and . The 3.5″ display area contains 480X(RGB)x800 pixels and can display up to 16.7M colors. This product accords with ROHS environmental criterion.

Item	Contents	Unit	Note
LCD Type	TFT	-	
Display color	16.7M		1
Viewing Direction	ALL	O'Clock	
Operating temperature	-20~+70	℃	
Storage temperature	-30~+80	℃	
Module size	51.32X86.61X2.1	mm	2
Active Area(W×H)	45.36X75.60	mm	
Number of Dots	480×800	dots	
Controller	ST7701S	-	
Power Supply Voltage	2.8	V	
Backlight	7S-LEDs (white)	pcs	
Weight	---	g	
Interface	SPI+RGB 24/18/16-bit	-	

Note 1:   Color tune is slightly changed by temperature and driving voltage.

Note 2:   Without FPC and Solder.With

4.1 Outline.Drawing

NOTES:
1. DISPLAY TYPE:3.5"TFT,TRANSMISSIVE
2. VIEWING DIRECTION:U/L/D/R 80/80/80/80
3. LCD DRIVER IC :ST7701S
4. Top : -20°C ~ 70°C,Tst:-30°C~80°C
5. LCM Luminance:LED/450cd/m²
6. GENERAL TOLERANCE: ±0.2
7. IN LINE WITH THE ROHS.

连接器型号 AWF35B35

25.07±0.5
2.00±0.3
68.23±0.5
8.83

86.61±0.2 BL
79.20±0.15 (POL)
75.60/LCD (A.A)
1.38
2.98

3.5 TFT
480(H)RGB×800(V)
View direction: All

51.32±0.2 BL
48.56±0.15 (POL)
45.36/LCD (A.A)
2.98
1.38

2.1±0.15 MAX
0.200.03(PI+FPC)

LEDA
IF=20mA,VF=21±2.1V
LEDK

2.50
20.00
D3.5.T.T=0.1MM
Conductive cloth T=0.1MM

LABEL
胶铁一体化

线槽压印线

背视图
FPC弯折出货

4-R0.20
4-R0.50
2.50±0.3
3.50±0.3
2.00±0.3
19.60

LABEL
胶铁一体化

线槽压印线

客户机壳注塑缝隙
FPC扣板弯度0.35MAX

0.8 MAX
20.60±0.5
22.77±0.5
0.20±0.3
(PI补强+FPC)

PIN	NAME	PIN	NAME	PIN	NAME	PIN	NAME
1	LEDK	14	SCL	27	LEDK		
2	LEDA	15	CS	28	LEDA		
3	NC	16	RESET	29	NC	42	VSYNC
4	NC	17	DB23	30	NC	43	HSYNC
5	NC	18	DB22	31	NC	44	VSYNC
6	GND	19	DB21	32	NC	45	NC
7	VCI	20	DB20	33	GND	46	NC
8	VCI	21	DB19	34	VCI	47	NC
9	NC	22	DB18	35	NC	48	NC
10	IOVCC	23	DB17	36	IOVCC	49	NC
11	TE	24	DB16	37	TE	50	NC
12	SDO	25	DB15	38	SDO	51	GND
13	SDI			39	SDI		

REVISE
V2
V1
V0
Revision note
First
DATE 2020.06.09
NAME

Victronix Tecenstar INT'L Co., LTD

ApprovedBy
CheckedBy
DrawnBy
UNIT:mm

SIZE: TFT_LCD 3.5"
DWARING_NO.:
CUSTOMER_NO.

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5. Absolute Maximum Ratings(Ta=25℃)

5.1 Electrical Absolute Maximum Ratings.(Vss=0V ,Ta=25℃)

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	VCI	-0.3	4.6	V	1，2
	IOVCC	-0.3	4.6		

Notes:1. If the module is above these absolute maximum ratings. It may become permanently damaged. Using the module within the following electrical characteristic conditions are also exceeded, the module will malfunction and cause poor reliability.

2. $V_{DVDD} > V_{SS}$ must be maintained.

3. Please be sure users are grounded when handing LCD Module.

5.2 Environmental Absolute Maximum Ratings.

Item	Storage		Operating		Note
	MIN.	MAX.	MIN.	MAX.	
Ambient Temperature	-30℃	80℃	-20℃	70℃	1,2
Humidity	-	-	-	-	3

1. The response time will become lower when operated at low temperature.

2. Background color changes slightly depending on ambient temperature.

   The phenomenon is reversible.

3. Ta<=40℃:85%RH MAX.

   Ta>=40℃:Absolute humidity must be lower than the humidity of 85%RH at 40℃.

6. Electrical Specifications

6.1 Electrical characteristics(Vss=0V ,Ta=25℃)

Parameter	Symbol	Condition	Min	Typ	Max	Unit	Note
Power supply	VCI	Ta=25°C	2.5	2.8	3.6	V	
	IOVCC	Ta=25°C	1.65	1.8	3.6	V	

Input voltage	'H'	V_{IH}	IOVCC=1.8V	0.7*IOVCC	-	IOVCC	V	
	'L'	V_{IL}	IOVCC=1.8V	0	-	0.3*IOVCC	V	

6.2 LED backlight specification(VSS=0V ,Ta=25°C)

Item	Symbol	Condition	Min	Typ	Max	Unit	Note
Supply voltage	V_f	If=20mA	18.9	-	23.1	V	
Uniformity	△Bp	If=20mA	75	80	-	%	
Life Time	time	If=20mA	20K	-		hours	1
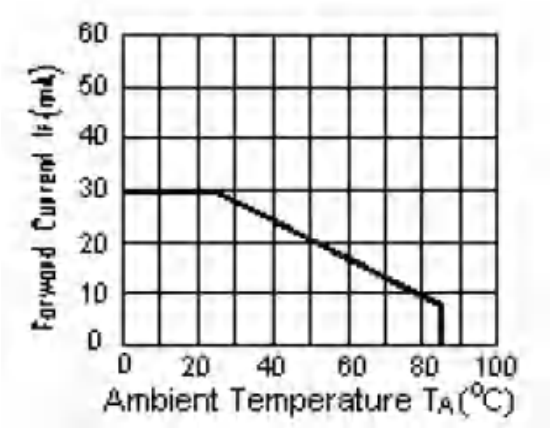
Note 1:   Brightness to be decreased to 50% of the initial value at ambient temperature TA=25 °C

6.3 Interface signals

Pin No.	Symbol	I/O	Function
1	LEDK	P	LED back light(Cathode)
2	LEDA	P	LED back light(Anode)
3	NC	-	No connection.
4	NC	-	
5	NC	-	
6	NC	-	
7	GND	P	Ground.
8	VCI	P	Power supply (2.8V)
9	NC	-	No connection.
10	IOVCC	P	Power supply for IO
11	TE	O	Tearing effect output pin to synchronize MPU to frame writing,
12	SDO	O	Serial output data bus
13	SDI	I	Serial Input data bus
14	SCL	I	Serial clock as serial interface.
15	CS	I	Chip select signal
16	RESET	I	Reset input pin ,active "L".
17-40	DB23-DB00	I	A 24-bit parallel data bus for RGB Interface. 24-bit/pixel: D[23:16]=R,D[15:8]=G,D[7:0]=B 18-bit/pixel: MDT=0:D[21:16]=R,D[13:8]=G,D[5:0]=B MDT=1:D[17:12]=R,D[11:6]=G,D[5:0]=B 16-bit/pixel: D[20:16]=R,D[13:8]=G,[4:0]=B *Fix in VDDI or DGND level when not in use.*
41	ENABLE	I	Data enable pin
42	DOTCLK	I	Data clock
43	HSYNC	I	Horizontal sync input in RGB mode
44	VSYNC	I	Vertical sync input in RGB mode
45-50	NC	-	No connection.
51	GND	P	Ground.

6.4 AC Characteristics

Serial Interface Characteristics (3-line serial):
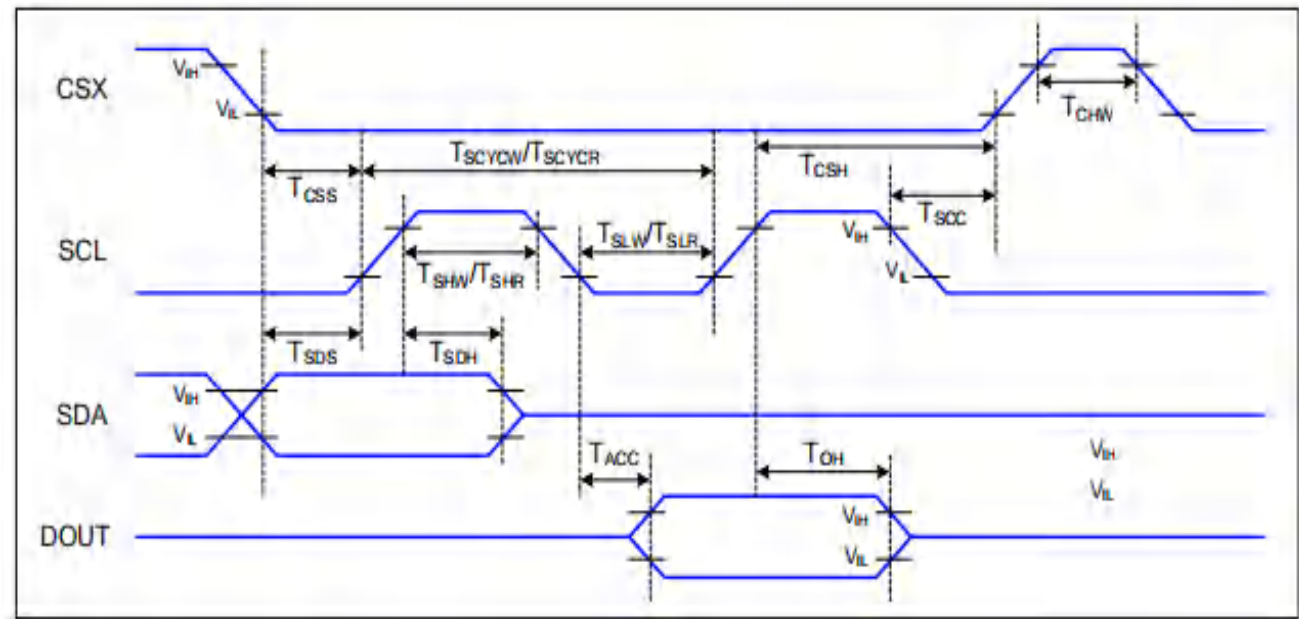
Signal	Symbol	Parameter	Min	Max	Unit	Description
CSX	T_{CSS}	Chip select setup time (write)	15		ns	
	T_{CSH}	Chip select hold time (write)	15		ns	
	T_{CSS}	Chip select setup time (read)	60		ns	
	T_{SCC}	Chip select hold time (read)	60		ns	
	T_{CHW}	Chip select "H" pulse width	40		ns	
SCL	T_{SCYCW}	Serial clock cycle (Write)	66		ns	
	T_{SHW}	SCL "H" pulse width (Write)	15		ns	
	T_{SLW}	SCL "L" pulse width (Write)	15		ns	
	T_{SCYCR}	Serial clock cycle (Read)	150		ns	
	T_{SHR}	SCL "H" pulse width (Read)	60		ns	
	T_{SLR}	SCL "L" pulse width (Read)	60		ns	
SDA	T_{SDS}	Data setup time	10		ns	
(DIN)	T_{SDH}	Data hold time	10		ns	

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RGB Interface Characteristics :
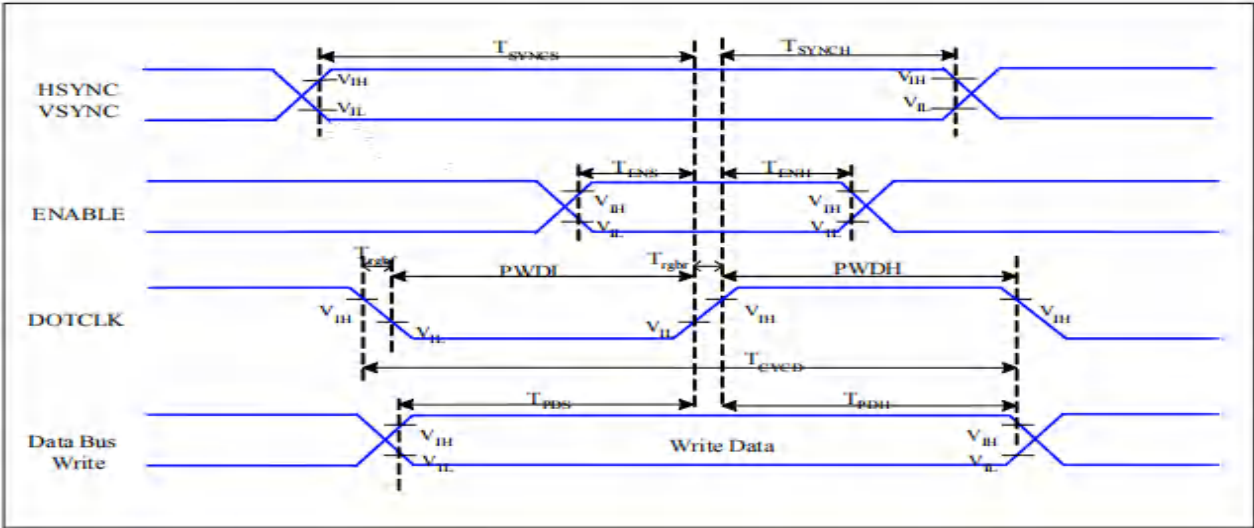
Signal	Symbol	Parameter	MIN	MAX	Unit	Description
HSYNC, VSYNC	T_{SYNCS}	VSYNC, HSYNC Setup Time	5	-	ns	
ENABLE	T_{ENS}	Enable Setup Time	5	-	ns	
	T_{ENH}	Enable Hold Time	5	-	ns	
DOTCLK	PWDH	DOTCLK High-level Pulse Width	15	-	ns	
	PWDL	DOTCLK Low-level Pulse Width	15	-	ns	
	T_{CYCD}	DOTCLK Cycle Time	33	-	ns	
	Trghr, Trghf	DOTCLK Rise/Fall time	-	15	ns	
DB	T_{PDS}	PD Data Setup Time	5	-	ns	
	T_{PDH}	PD Data Hold Time	5	-	ns	

6.5 Reset timing
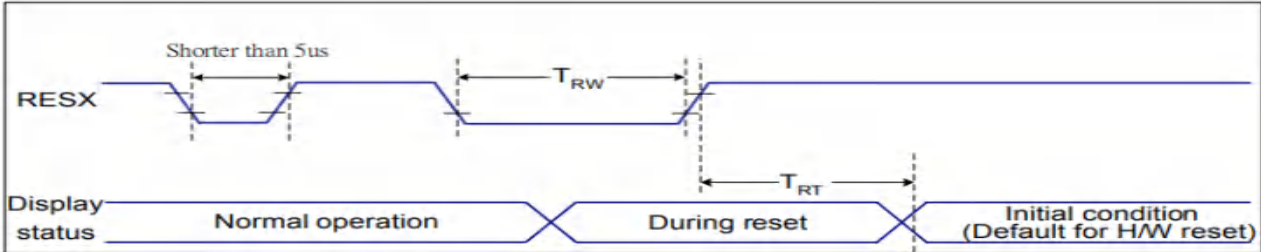
Related Pins	Symbol	Parameter	MIN	MAX	Unit
RESX	TRW	Reset pulse duration	10	-	us
	TRT	Reset cancel	-	5 (Note 1, 5)	ms
				120(Note 1, 6, 7)	ms

7. *Optical Characteristics*

Item	Symbol		Condition	Min.	Typ.	Max.	Unit	Note
Brightness	Bp		$\theta=0°$ $\Phi=0°$		450	-	Cd/m^2	1
Uniformity	⊿Bp			-	85	-	%	1,2
Viewing Angle	3:00		Cr≥10	-	80	-	Deg	3
	6:00			-	80	-		
	9:00			-	80	-		
	12:00			-	80	-		
Contrast Ratio	Cr		$\theta=0°$ $\Phi=0°$	800	1000	-	-	4
Response Time	$T_{r+}T_f$			-	25	40	ms	
Color of CIE Coordinate	W	x	$\theta=0°$ $\Phi=0°$	TYP -0.05	0.315	TYP +0.05	-	1,6
		y			0.335		-	
	R	x			0.620		-	
		y			0.327		-	
	G	x			0.302		-	
		y			0.591		-	
	B	x			0.138		-	
		y			0.104		-	
NTSC Ratio	S			64	69	-	%	

Note：*The parameter is slightly changed by temperature, driving voltage and materiel*

Note 1: *The data are measured after LEDs are turned on for 5 minutes. LCM displays full white. The*

brightness is the average value of 9 measured spots. Measurement equipment BM-7 (Φ5mm)

Measuring condition:

- *Measuring surroundings: Dark room.*

- *Measuring temperature: Ta=25℃.*

- *Adjust operating voltage to get optimum contrast at the center of the display.*

*Measured value at the center point of LCD panel after more than 5 minutes while backlight*

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turning on.
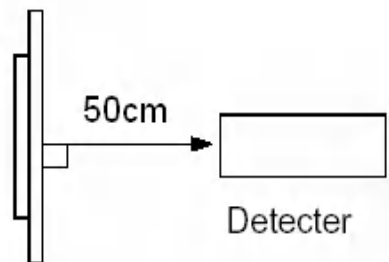
Note 2: The luminance uniformity is calculated by using following formula.
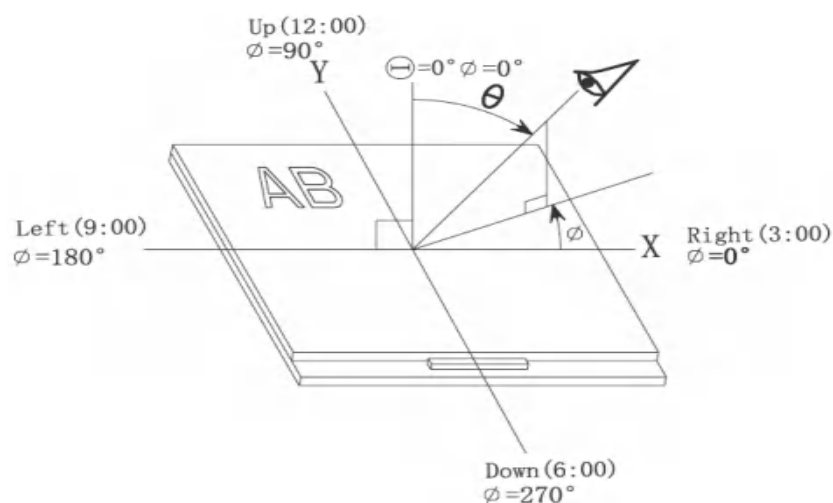
$\angle Bp = Bp\ (Min.) / Bp\ (Max.) \times 100\ (\%)$

Bp (Max.) = Maximum brightness in 9 measured spots

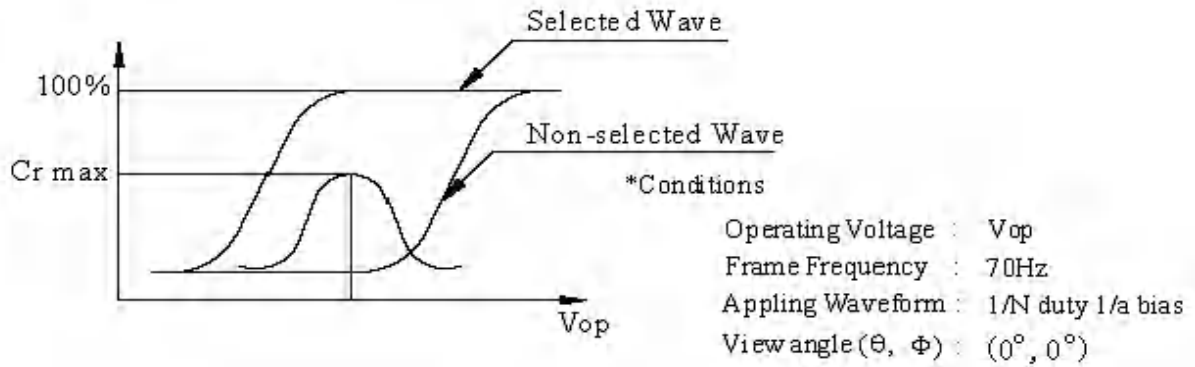*Bp (Min.) = Minimum brightness in 9 measured spots.*
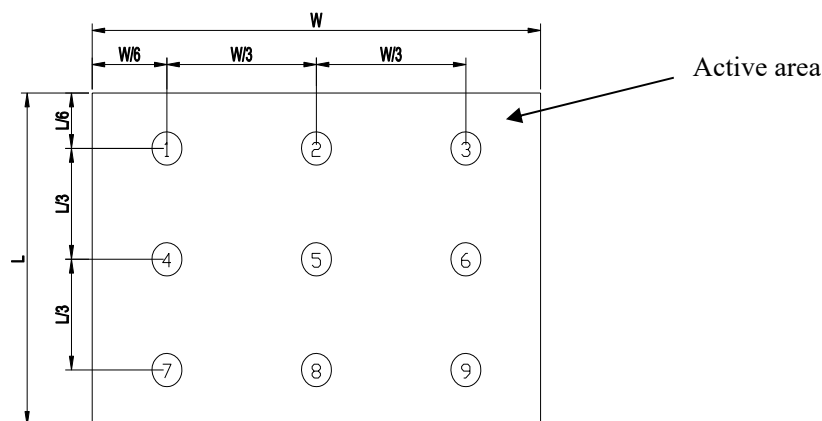
Note 3: The definition of viewing angle:
  Refer to the graph below marked by ϑ and Φ


Note 4: Definition of contrast ratio.(Test LCD using DMS501)

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$$Contrast\ ratio(Cr) = \frac{Brightness\ of\ selected\ dots}{Brightness\ of\ non-selected\ dots}$$

Note 5: Definition of Response time. (Test LCD using DMS501):

The output signals of photo detector are measured when the input signals are changed from"black"to"white"(rising time) and from" white"to "black"(falling time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes.Refer to figure as below.
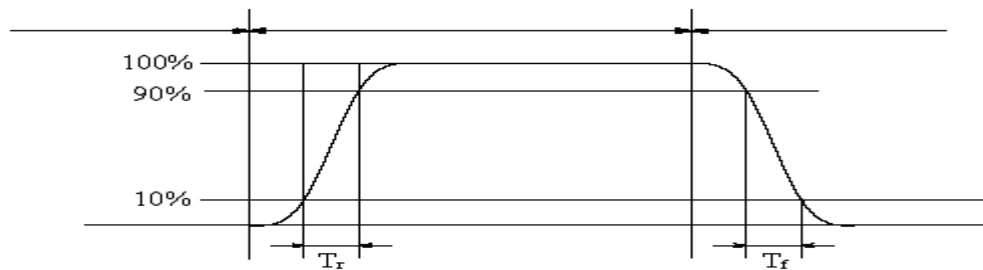
The definition of response time

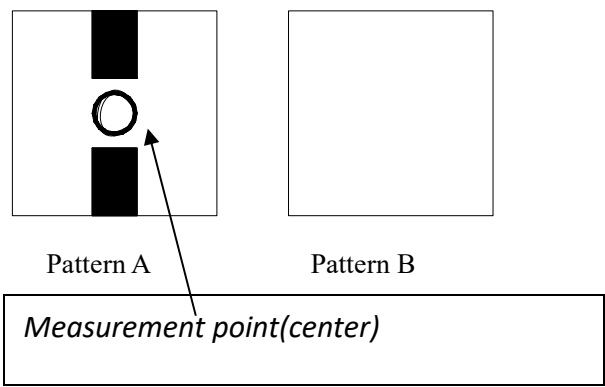*Note 6: Definition of Color of CIE Coordinate and NTSC Ratio.*

1931 CIE Chromaticity Diagram
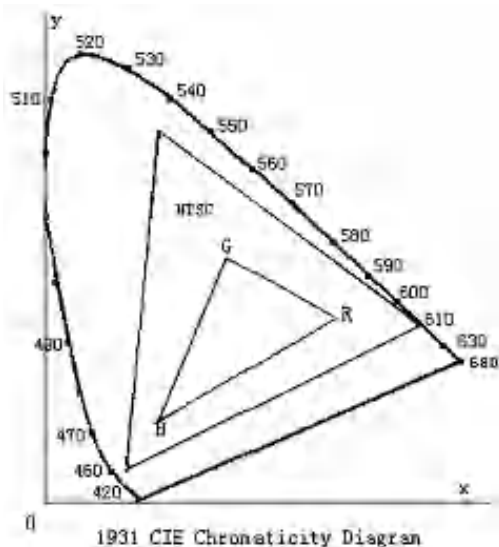
Color gamut:

$$S = \frac{area\ of\ RGB\ triangle}{area\ of\ NTSC\ triangle} \times 100\%$$

Note 7: Definition of cross talk.

*Cross talk ratio(%)=|pattern A Brightness-pattern B Brightness|/pattern A Brightness*100*

12/16

Pattern A   Pattern B

Measurement point(center)

Electric volume value=3F+/-3Hex

13/16

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8. Reliability Test Items and Criteria

Test Item	Test condition	Remark
High Temperature Storage	Ta = 80℃ 96hrs	Note1,Note3,4
Low Temperature Storage	Ta = -30℃ 96hrs	Note1,Note3,4
High Temperature Operation	Ts = 70℃ 96hrs	Note2,Note3,4
Low Temperature Operation	Ta = -20℃ 96hrs	Note1,Note3,4
Operation at High Temperature/Humidity	+60℃, 90%RH 96hrs	Note3,4
Thermal Shock	-30℃/30 min ~ +80℃/30 min for a total 100 cycles, Start with cold temperature and end with high temperature.	Note3,4
Vibration Test	Frequency range:10~55Hz Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X. Y. Z. (6 hours for total)	
Mechanical Shock	100G 6ms,±X, ±Y, ±Z 3 times for each direction	
Package Vibration Test	Random Vibration : 0.015G*G/Hz from 5-200HZ, -6dB/Octave from 200-500HZ 2 hours for each direction of X. Y. Z. (6 hours for total)	
Package Drop Test	Height:60cm 1 corner, 3 edges, 6 surfaces	
Electro Static Discharge	±2KV, Human Body Mode, 100pF/1500Ω	

Note 1: Ta is the ambient temperature of samples.

Note 2: Ts is the temperature of panel's surface.

Note 3: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but don't guarantee all of the cosmetic specification.

Note 4: Before cosmetic and function test, the product must have enough recovery time,at least 2 hours at room temperature

9. *Precautions for Use of LCD Modules*

9.1 Handling Precautions

9.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

9.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

9.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

9.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

9.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

   — Isopropyl alcohol   — Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer.   Especially, do not use the following:

   — Water   — Ketone   — Aromatic solvents

9.1.6 Do not attempt to disassemble the LCD Module.

9.1.7 If the logic circuit power is off, do not apply the input signals.

9.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

   a. Be sure to ground the body when handling the LCD Modules.

   b. Tools required for assembly, such as soldering irons, must be properly ground.

   c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

   d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off

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this protective film since static electricity may be generated.

9.2 Storage precautions

9.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

9.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature :　　　　　0℃ ～ 40℃

Relatively humidity:　≤80%

9.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

9.3 *The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.*